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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2829

Examiner: Velez, Roberto

In Re PATENT APPLICATION Of:

Applicants : Toshio TERAISHI

Serial No. : 10/617,817

Filed : July 14, 2003

For : ANALOG VOLTAGE OUTPUT DRIVER
LSI CHIP HAVING TEST CIRCUIT

Attorney Ref. : 03DCOAI030

**Response to
Restriction
Requirement**

September 22, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Sir:

Responsive to the Restriction Requirement dated August 29, 2006, Applicant elects the invention of Species I, i.e., the subject matter of claims 1-3 and 12-15, for further examination. The election is made without traverse.

Examination of the application on the merits is respectfully requested.

Respectfully submitted,

September 22 2006
Date


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